

Introducing New Paradigms in Basic Control Education Using the YOULA Parameterisation

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Abstract: Course material of basic control theory has been overviewed and updated recently at the Faculty of Electrical Engineering and Informatics, BUTE. In the theoretical material the concept of the YOULA parameterisation has been introduced which gives a new insight into controller design. New lecture notes were written both for the theoretical material and for the MATLAB laboratory exercises. An example demonstrates the design procedure and the gaining effect of robustness of the filters in case of plant/model mismatch.

Keywords: control education, YOULA parameterisation, YOULA regulator, sensitivity

1. Introduction

At the Faculty of Electrical Engineering and Informatics, Budapest University of Technology and Economics, control theory is taught as a basic discipline for all students specialized in informatics. The subject offers fundamental knowledge in analysis and design of continuous and sampled data control systems. The course material has been overviewed and updated recently. Newer ideas for controller design as YOULA parameterisation has also been introduced which gives a new insight into controller design. It is shown that control algorithms like *PID*, dead-beat and Smith predictor control can be considered as special cases of YOULA parameterisation. New university lecture notes were written providing the theoretical material (Keviczky and Bányász (2015), Keviczky etc. (2006, 2009)) and the related MATLAB exercises.

2. Understanding control concepts, introducing the idea of Youla parameterisation

In the sequel it will be shown how the concept of closed-loop control is introduced and how it is related then to the YOULA parameterisation (Keviczky and Bányász (2015)). Control theory deals with the analysis and design of closed loop control systems. The main control structure is based on negative feedback. The goal in control of physical plants is to track the output signal according to a reference signal and to reject the effect of the disturbances. There are requirements set to the performance of the control system. First it has to be stable, then, it has to meet the quality specifications set for steady-state accuracy, dynamic properties such as overshoot, settling time, etc. The control signal has to be inside its technical limits. The control system has to be not very sensitive to measurement noises and to plant/model mismatch. It has to be also technically realizable and eligible to economical and other (e.g. environmental protection) viewpoints.

The control is realized through negative feedback if the input signal (the manipulated variable) of the process is affected by the difference of the measured output signal and its desired prescribed value. The measured output value is generally noisy because of the noise acting on the measurement equipment. Based on the error signal e the controller C generates the manipulated variable u , which modifies the output signal of the process P . The process itself is supposed to be stable. The output signal of the process is changing according to the dynamics of the control circuit

until it reaches its desired value. The block-diagram of the closed-loop control system is given in Fig. 1. Often the reference signal is filtered by a pre-compensator element of transfer function (denoted by dotted line in the figure).

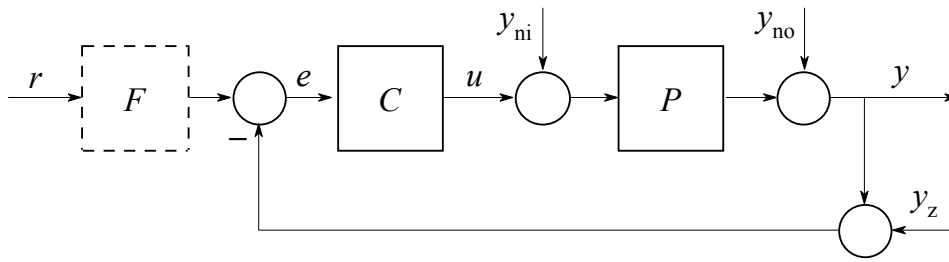


Fig.1. Closed-loop control circuit

If the disturbances and the measurement noise are not considered and the filter is supposed to be unity ($F=1$), then the open loop circuit shown in Fig. 2. is equivalent to Fig. 1., regarding reference signal tracking.

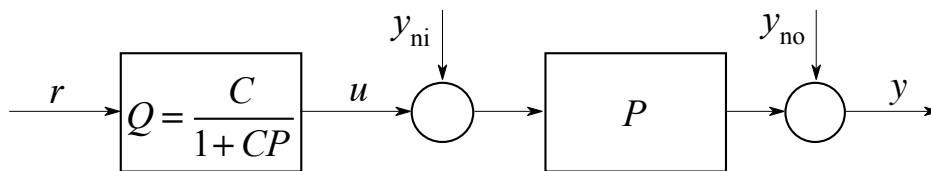


Fig.2. Equivalent open-loop structure

Here Q is the YOULA parameter. The classical YOULA parameterisation gives a very simple way for open-loop stable processes when the regulator can be analytically designed by explicit formulas. The YOULA parameter is, as a matter of fact, a stable regular transfer function. By definition

$$Q(s) = \frac{U(s)}{R(s)} = \frac{C(s)}{1 + C(s)P(s)} \quad \text{or shortly} \quad Q = \frac{C}{1 + CP} \quad (1)$$

where $C(s)$ is a stabilizing regulator, and $P(s)$ is the transfer function of the stable process.

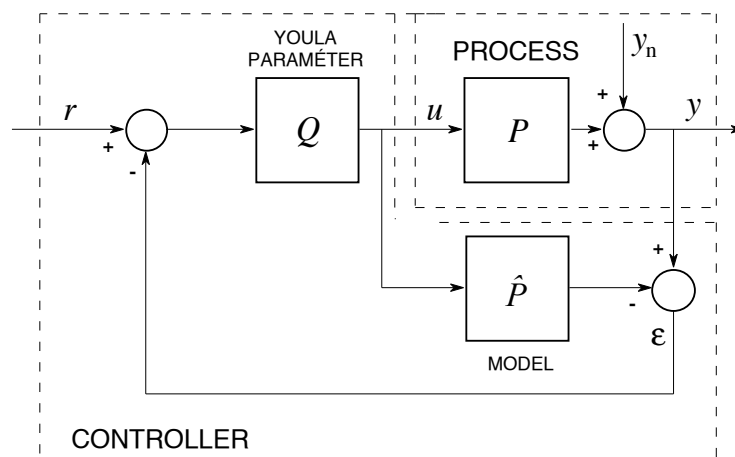


Fig. 3. YOULA parameterised control system with IMC

If the process P is stable, then all stable Q controllers ensure stable control system. Similar relationships are obtained for discrete systems as well where instead of the transfer functions the pulse transfer functions are considered.

The open loop structure shown in Fig. 2 ensures reference signal tracking but does not reject the effect of disturbances.

To ensure disturbance rejection as well the open-loop control structure is extended by *IMC* according to Fig.3.

Fig. 4 shows an equivalent block diagram supposing that the model is equal to the system, $\hat{P} = P$. In this usual feedback structure the controller C is expressed by the Q YOULA parameter

$$C(s) = \frac{Q(s)}{1 - Q(s)P(s)}$$

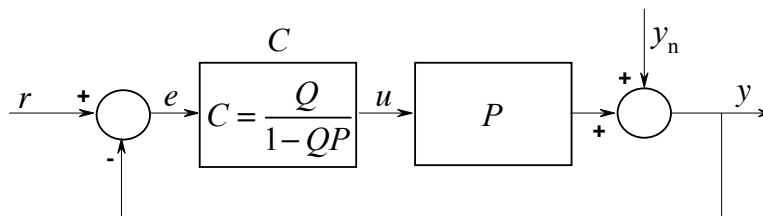


Fig. 4. The usual feedback system with the YOULA parameter in the controller

The best reference signal tracking, when the output signal is exactly equal to the reference signal could be reached if the YOULA parameter is the inverse of the transfer function of the process: $Q = P^{-1}$.

But generally this condition cannot be fulfilled. The dead time of the process cannot be inverted as its inverse is not realizable. It is also not realizable if the numerator of the inverse is of higher degree than that of its denominator. Right side zeros of the transfer function cannot be inverted either, as they will produce unstable poles in the controller. For discrete systems zeros outside of the unit circle cannot be inverted, and cancellation of zeros which lie on the left side of the unit circle (or in the undesired part of the unit circle) is to be avoided as their inversion would cause inter-sampling oscillation.

Therefore Q can be only the inverse of the invertible part of the transfer function of the plant. Let us separate the plant transfer function to the invertible $P_+(s)$ and the noninvertible $\bar{P}_-(s)$ factors, where the latter contains also the dead time.

$$P(s) = P_+(s)\bar{P}_-(s)$$

Then $Q = P_+^{-1}$. The gain of $\bar{P}_-(s)$ has to be 1 as this determines the static gain in the forward path. Figure 5 shows now the *IMC* control structure.

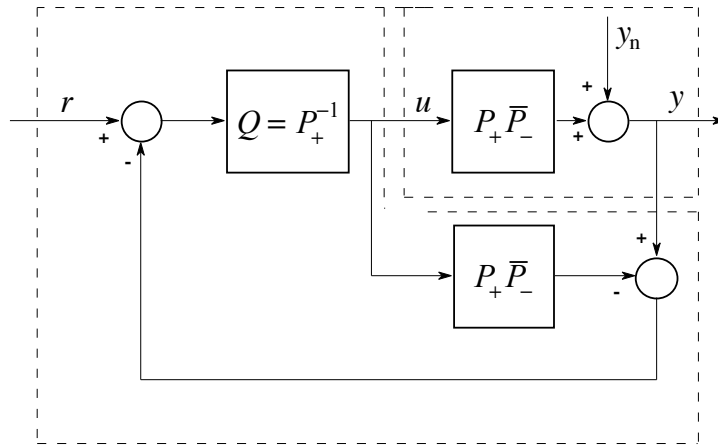


Fig. 5. Realizable YOULA parameterised *IMC* control structure

In this configuration the dynamics of reference signal tracking and disturbance rejection is the same.

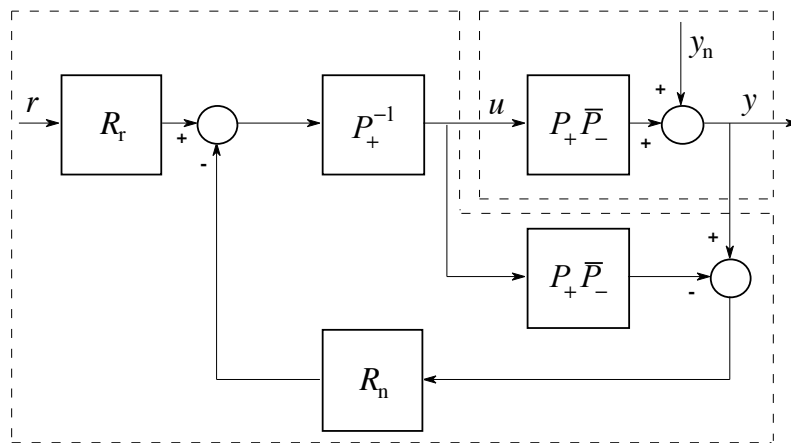


Fig. 6. YOULA parameterised control with filters

If different dynamics are required (e.g., disturbance rejection has to be faster than reference signal tracking), then reference and disturbance filters can be used with unity gain as shown in Fig. 6. This structure is called *2DOF* (two-degree-of-freedom) structure.

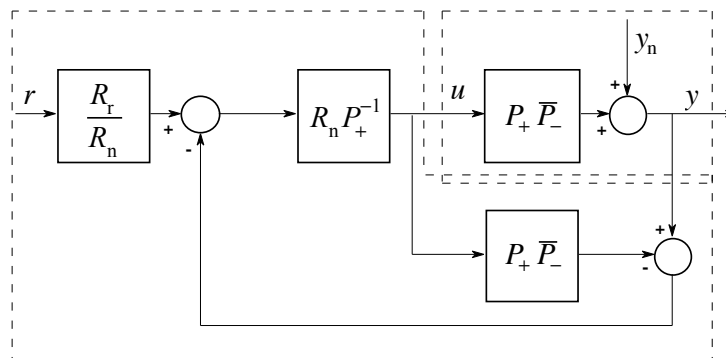


Fig. 7. Equivalent YOULA parameterised *IMC* control system

Equivalent structures are shown in Figs. 7 and 8. Now the YOULA parameter is $Q = R_n P_+^{-1}$.

The series controller is $C = \frac{R_n P_+^{-1}}{1 - R_n P_-}$. For discrete systems the relationships are similar with the z -transforms.

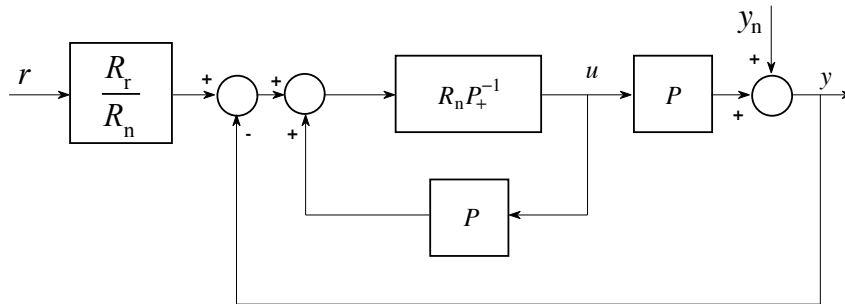


Fig. 8. Equivalent YOULA parameterised control system

Besides ensuring different dynamics for reference signal tracking and disturbance rejection another role of the filters is to modify the value of the control signal u keeping it inside the allowed limits. The filters have also a gaining effect of robustness. With their appropriate choice the control system can be done less sensitive to plant/model mismatch.

Summarizing the design procedure: The plant transfer function has to be separated into its invertible and non-invertible parts. The reference and disturbance filters have to be given as design objectives. The controller can be designed in open-loop, ensuring the best realizable reference signal tracking. Disturbance rejection is provided by enhancing the control with Internal Model Control (*IMC*) structure. The filters have to be chosen considering robustifying criteria.

3. Simulation Example

The transfer function of the continuous plant is

$$P(s) = \frac{1}{(1+5s)(1+10s)} e^{-30s}$$

It is sampled, and zero order hold is applied at the input. The sampling time is $T_s = 5$ sec. The corresponding pulse transfer function is

$$P(z) = \frac{0.1548(z+0.6065)}{(z-0.3679)(z-0.6065)} z^{-6}$$

First a *PID* controller is designed for pole cancellation (cancelling the biggest pole of the system and introducing an integrating effect instead, and cancelling also the second pole introducing a differentiation instead) and for phase margin about $\phi_m \approx 60^\circ$. The pulse transfer function of the controller is

$$C(z) = 0.3074 \frac{z-0.6065}{z-1} \frac{z-0.3679}{z}$$

Let us design a YOULA controller first without filters, $R_r = R_n = 1$.

Let us separate the pulse transfer function of the plant into invertible and non-invertible parts.

$$\bar{P}_-(z^{-1}) = \frac{(1 + 0.6065 z^{-1})z^{-1}}{1.6065} z^{-6}$$

$$P_+(z^{-1}) = \frac{0.1548 \cdot 1.6065}{(1 - 0.3679 z^{-1})(1 - 0.6065 z^{-1})}$$

and the YOULA parameter is

$$Q(z^{-1}) = \frac{(1 - 0.3679 z^{-1})(1 - 0.6065 z^{-1})}{0.1548 \cdot 1.6065}$$

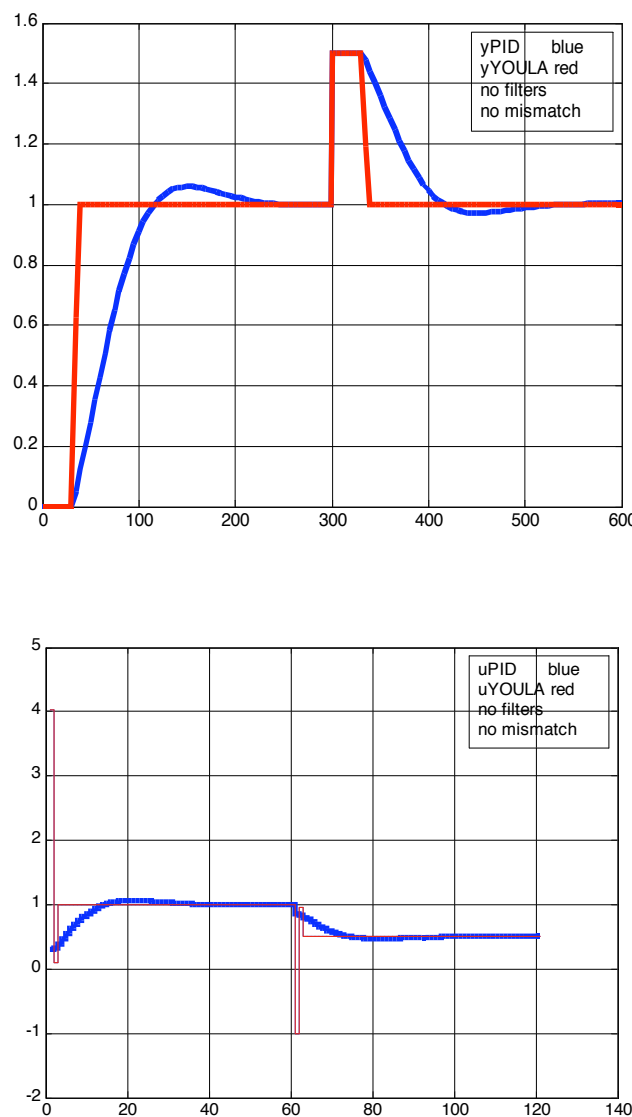


Fig. 9. Output signals (upper figure) and control signals (lower figure) for PID (blue) and Youla (red) control for step input and output disturbance (no filters, no mismatch).

With first-order lag element filters $R_r(s) = \frac{1}{1+s}$ whose pulse transfer function is

$$R_r(z^{-1}) = \frac{0.9933z^{-1}}{1-0.006738z^{-1}} \text{ and } R_n(s) = \frac{1}{1+25s} \text{ whose pulse transfer function is}$$

$$R_n(z^{-1}) = \frac{0.1813z^{-1}}{1-0.8187z^{-1}}$$

the YOULA parameter is

$$Q = \frac{0.729(1-0.3679z^{-1})(1-0.6065z^{-1})z^{-1}}{1-0.8187z^{-1}}$$

Fig. 9 shows the output and control signal responses of *PID* and YOULA control with no filters and no plant/model mismatch. The step reference signal acts at $t = 0$ sec, and a step disturbance of 0.5 amplitude acts at $t = 300$ sec. It is seen that YOULA control is much faster because of the higher control signal. Fig.10 gives the responses with the filters. It is seen that in the response of the YOULA parameterised controller the dynamics is different for reference signal tracking and for disturbance rejection.

Let us consider the control behaviour in case of plant/model mismatch. The dead time of the system is 40 sec, while in the model 30 sec is considered and the controller has been designed based on this model. The *PID* controller still tolerates this uncertainty, but without the filters the YOULA controller becomes unstable (Fig. 11.). With the given filters its behaviour is acceptable (Fig. 12.).

4. Robustness Considerations for Dead Time Mismatch

Keviczky and Bányász (2012) analysed the relationship of performance and robustness, especially for the case of dead time mismatch (Keviczky etc. (2011)). In this case the relative model error is

$$\frac{P - \hat{P}}{\hat{P}} = \frac{\Delta}{\hat{P}} = \frac{P_+ e^{-sT_d} - P_+ e^{-s\hat{T}_d}}{P_+ e^{-s\hat{T}_d}} = e^{-\Delta T_d s} - 1$$

and P is the real process and \hat{P} is its model. It is supposed that the transfer function of the process without the dead time is accurately known, $\hat{P}_+ = P_+$ and $\Delta T_d = T_d - \hat{T}_d$

For robust stability

$$\sup_{\omega} |e^{-j\Delta T_d \omega} - 1| \leq \frac{1}{|R_n(\omega)|}$$

With first-order lag disturbance filter with time constant T_n this condition is expressed as

$$\left| 1 - \frac{\hat{T}_d}{T_d} \right| < \frac{\pi}{\sqrt{3}} \frac{T_n}{T_d}$$

With Taylor expansion of the exponential term a simpler robustness condition is obtained as

$$\left| 1 - \frac{\hat{T}_d}{T_d} \right| < \frac{T_n}{T_d}$$

In our example the above condition $|1 - 30 / 40| < 25 / 40$ is fulfilled.

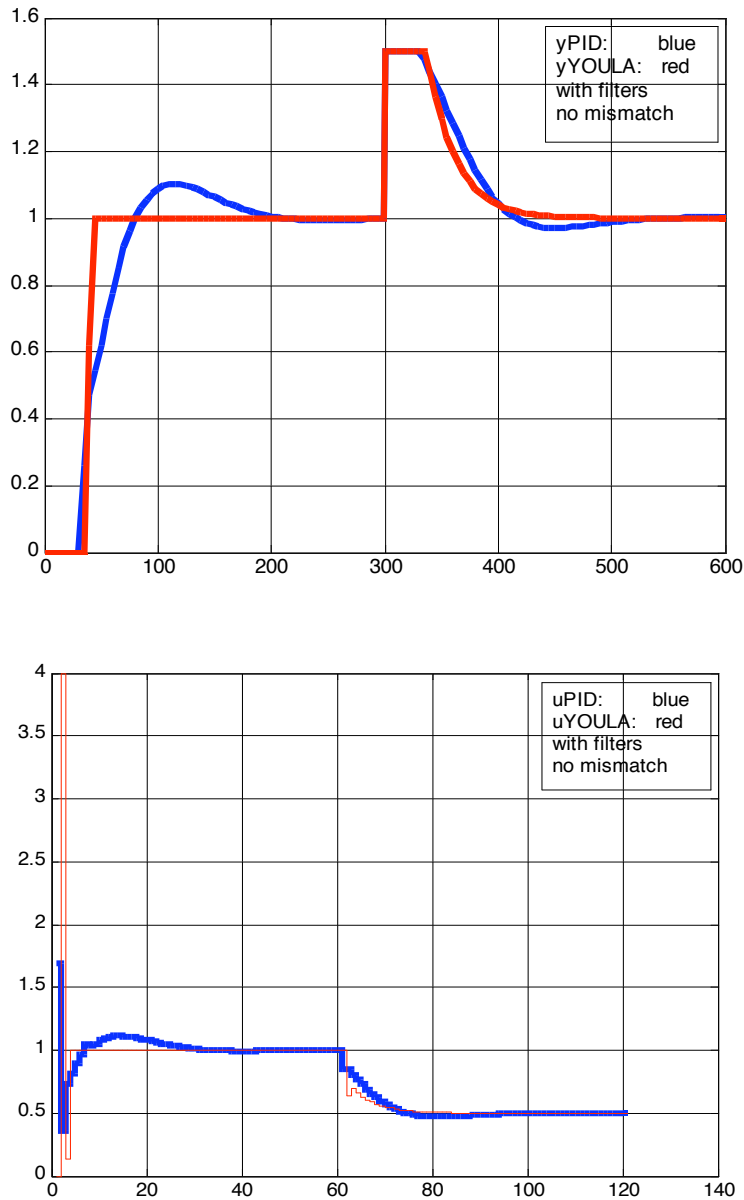


Fig. 10. Output signals (upper figure) and control signals (lower figure) for *PID* (blue) and *YOULA* (red) control for step input and output disturbance (with filters, no mismatch)

Fig. 13 shows the output signal for $T_n = 8$ sec, when the required condition is not fulfilled. In this case the output signal is oscillatory, the control system does not tolerate the mismatch in the dead time. Fig. 14 gives the output signal with $T_n = 15$ and 40 sec time constants of the disturbance filter. In these cases the control performance is improved significantly.

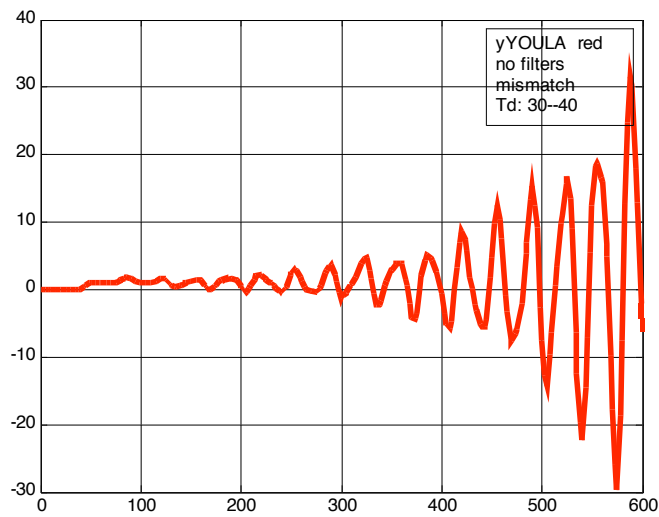
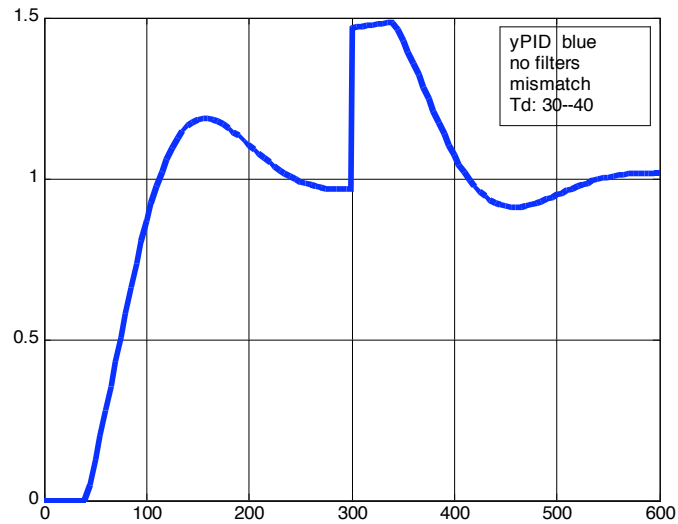


Fig. 11. With mismatch without filters *PID* control is stable, but the YOULA controller becomes unstable

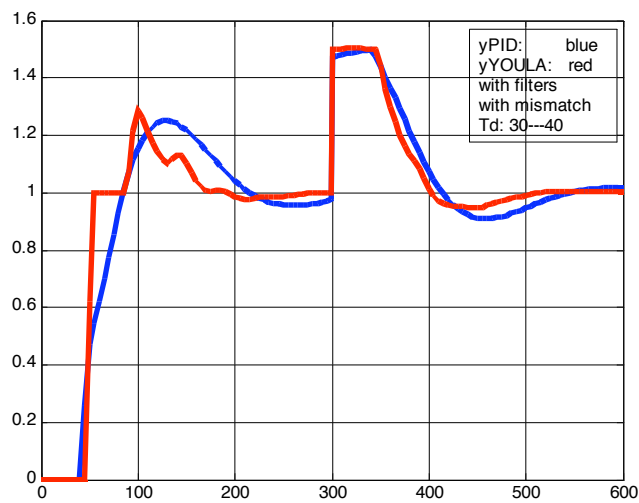


Fig. 12. With mismatch and with filters both *PID* control and the YOULA controller are stable

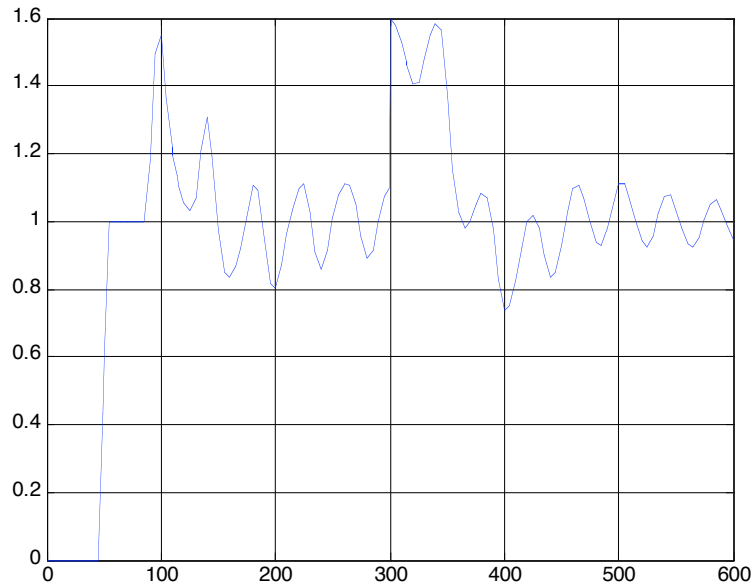


Fig. 13 With $T_n = 8$ oscillations appear in the output signal

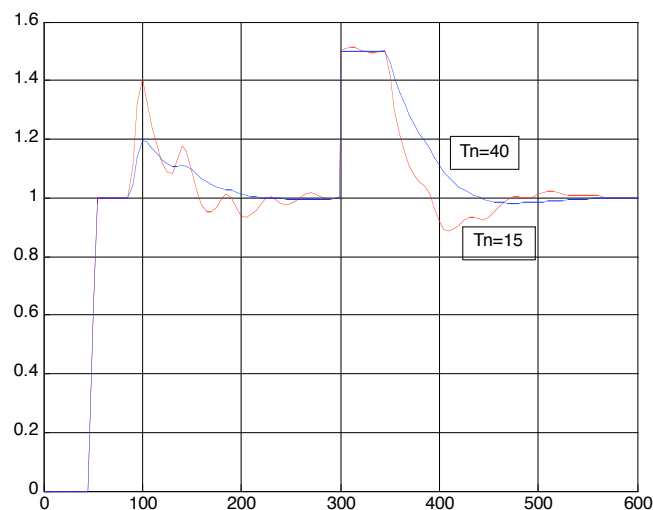


Fig.14. With appropriately chosen disturbance filter the control behaviour can be accepted

5. Conclusion

Youla parameterisation is a very effective control algorithm for control of stable processes. Keviczky and Bányász have researched the structure and several properties of this control paradigm. The controller can be designed in open loop providing the best realizable reference signal tracking, and extending the control system with feedback from the internal model (*IMC*) ensures disturbance rejection. Reference and disturbance filters modify the dynamic behaviour, thus the transients for reference signal tracking and disturbance rejection can be different. Appropriately chosen filters make robust the control behaviour in case of plant/model mismatch and also affect the maximum value of the control signal. It can be shown, that well known controllers like *PID*, dead beat, Smith predictor are special cases of YOUILA parameterisation.

This newer approach has been introduced in control education at the Faculty of Electrical Engineering and Informatics, BUTE. The theory is demonstrated through examples in the computer labs using software MATLAB/SIMULINK..

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